



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Marshall Isman  
Serial No. : 08/997,142  
Filed : December 23, 1997  
Title : METHOD FOR ANALYZING CAPACITY OF PARALLEL PROCESSING  
SYSTEMS

Art Unit : 2762  
Examiner : W. Zhen

Commissioner for Patents  
Washington, D.C. 20231

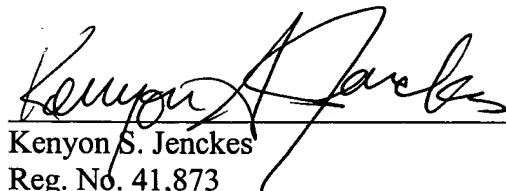
TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	12	-	20	=	0	\$0
Independent	6	-	6	=	0	\$0
TOTAL FEE DUE						\$0

Respectfully submitted,

Date: ~~7/27/00~~ 7/26/00 <sup>KSJ</sup>

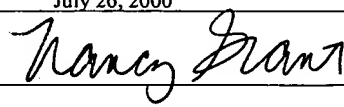
  
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## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Typed or Printed Name of Person Signing Certificate